Docket: 5853-268 UF No.: 10797

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

INTEGRATED VOLTAGE BOOST CIRCUIT FOR SIMPLIFIED BOARD DESIGN

INTE	GRAIE	D VOLTAGE BOOST	CIRCUIT FOR SIMPLIFIED BOARD	DESIGN
the specificat	tion of w	hich (check one)	·	
		is attached hereto. was filed on Application Serial No. was amended on	and	
	•		and understand the contents of the abended by any amendment referred to a	
	_	-	information which is material to the exode of Federal Regulations Section 1.5	
foreign applic below any for	cation(s reign ap	for patent or inventor	rits under Title 35, United States Code r's certificate listed below and have als inventor's certificate having a filing da l:	o identified
Prior Foreign	Applica	ation(s)	F	Priority Claimed
(Number)		(Country)	(Day/Month/Year Filed)	_ Yes No
(Number)		(Country)	(Day/Month/Year Filed)	_ Yes No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any U.S. application listed below and identified as a provisional application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior U.S. application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information material to the patentability of this application as defined in Title 37,

Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

Prior U.S.	Provisional	Application((s))
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(Appln. Serial No.)	(Filing Date)
60/415,618	October 2, 2002

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Appln. Serial No.)	(Filing Date)	(Status-patent, pending, abandoned)
(Appln. Serial No.)	(Filing Date)	(Status-patent, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Send correspondence to Terry W. Forsythe, Esq., Akerman, Senterfitt, 222 Lakeview Avenue, 4th Floor, P.O. Box 3188, West Palm Beach, Florida 33402-3188 and direct all telephone calls to Terry W. Forsythe, Esq. at (561) 653-5000.

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FULL NAME OF INVENTOR: WILLIAM R. EISENSTADT		
INVENTOR'S SIGNATURE:	_ DATE:	
RESIDENCE: 4315 NW 75 th St., GAINESVILLE, FL 32606		
CITIZENSHIP: US		